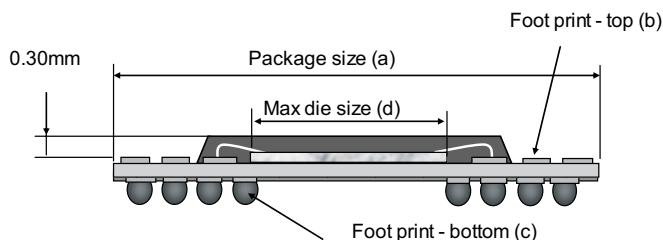
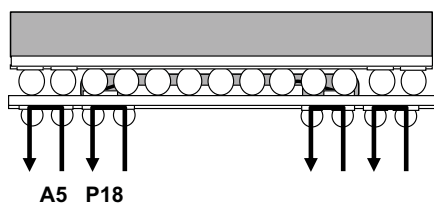


15mm 604 PSvfBGA Bottom Package Design Dimensions

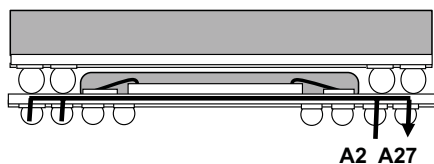


Body (a)	Foot Print-top (b)	Foot Print-bottom (c)
15 x 15mm	0.65 pitch, 160 ball 22 matrix, 2 row	0.5 pitch 28 matrix, 3 + 2 + 2 + 8 Central 604 BGA

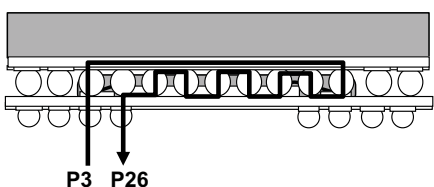
PoP Daisy Chain 3 Net Design



Daisy chain netlist of PSvfBGA, Bottom package balls, excluding 24 corner balls



Daisy chain netlist of bottom package 6 x 4 corner balls



Daisy chain netlist of top side (Top FBGA to PSvfBGA 160 pin memory interface)

Bottom package called:
Package Stackable very thin fine pitch BGA (PSvfBGA)

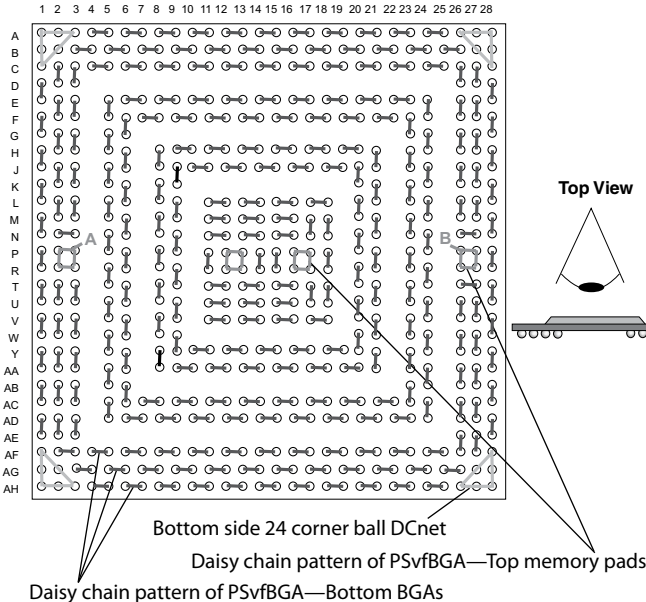
Package on Package (PoP)

15x15mm Stacked Daisy Chain

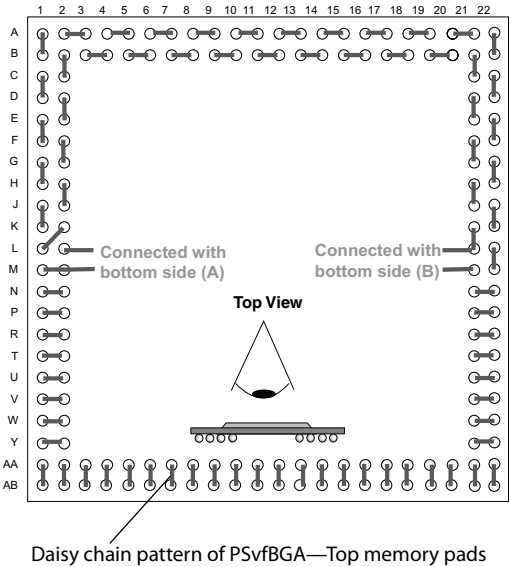


PSvfBGA 604 (Bottom Package) Daisy Chain Nets

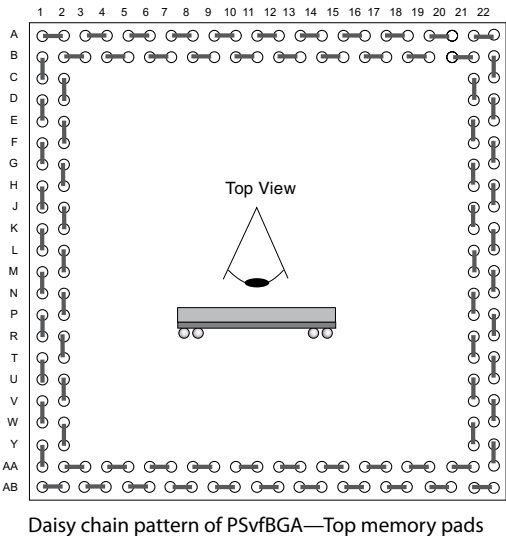
Bottom side (top view through package)
15x15mm, 0.5 mm PSvfBGA604, 28x28 ball matrix



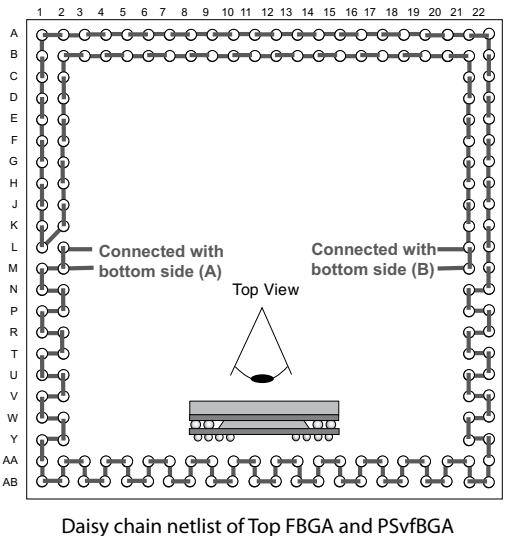
Top side—Top Package interface (top view)
0.65mm pitch, 160 pads, 22x22 ball matrix



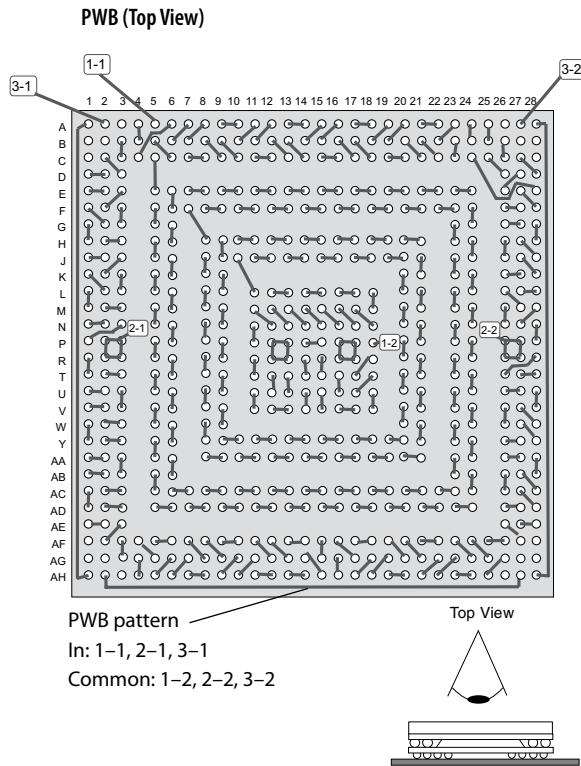
160 FBGA (Top Package) Daisy Chain Netlist



Top FBGA + PSvfBGA Daisy Chain Netlist



PWB Netlist Pattern for BLR Testing



Stacked view of 3 DC Nets for BLR testing

