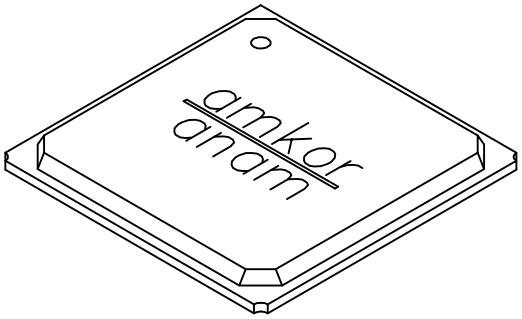
REVISION HISTORY					
REV	DCN NUMBER	DESCRIPTION	ORIGINATOR	INCORPORATOR	DATE
00	C9985	INITIAL ENGINEERING REVIEW	EHUDS	DARNO	08/29/00



8. REFERENCE SPECIFICATIONS:
   A. AAWW SPEC #001-0531-2234: PACKING OPERATION PROCEDURE.
   B. AAWW SPEC #001-0519-2062: MARKING.
   C. THIS DRAWING CONFORMS TO THE JEDEC REGISTERED OUTLINE MS-034/A, VARIATION AAG-2 EXCEPT FOR A1, b, AND aaa DIMENSIONS.

JEDEC(AMKOR) DIM	MIN	NOM	MAX
A1	0.40(0.30)	0.50(0.40)	0.60(0.50)
b	0.50(0.40)	0.60(0.50)	0.70(0.60)
aaa		0.20(0.15)	

7. A1 BALL PAD CORNER I.D. FOR PLATE MOLD: TO BE MARKED BY INK.
   AUTO MOLD: DIMPLE TO BE FORMED BY MOLD CAP.

6. PRIMARY DATUM C AND SEATING PLANE ARE DEFINED BY THE SPHERICAL CROWNS OF THE SOLDER BALLS.

5. DIMENSION IS MEASURED AT THE MAXIMUM SOLDER BALL DIAMETER, PARALLEL TO PRIMARY DATUM C.

4. THE MAXIMUM ALLOWABLE NUMBER OF SOLDER BALLS IS 289.

3. THE MAXIMUM SOLDER BALL MATRIX SIZE IS 17 X 17.

2. THE BASIC SOLDER BALL GRID PITCH IS 1.00mm.

1. ALL DIMENSIONS AND TOLERANCES CONFORM TO ASME Y14.5M-1994.

NOTES: UNLESS OTHERWISE SPECIFIED

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UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN MILLIMETERS
DECIMAL: X.X ±0.1, X.XX ±0.05, X.XXX ±0.030
ANGULAR ±1°
INTERPRET DIM AND TOL PER ASME Y14.5M - 1994

MATERIAL: N/A
FINISH: N/A
DO NOT SCALE DRAWING

THIRD ANGLE PROJECTION

APPROVALS		DATE
DESIGNED	EHUDS	08/25/00
CHECKED	JSILL	08/25/00
PRODUCT MANAGER	BGOGU	08/25/00
RELEASED	RHOLM	08/29/00

Amkor Technology, Inc.
Chandler, Arizona, USA
Anam Semiconductor, Inc.,
Seoul, Korea
Amkor Technology Philippines, Inc.
Manila, Philippines

Amkor Technology / ANAM Semiconductor

TITLE: PACKAGE OUTLINE - PBGA, 17 X 17 MATRIX, 19.00mm X 19.00mm, 17.70 X 17.70 X 0.80mm MOLD CAP, 1.00mm PITCH, LAMINATE SUBSTRATE, AAWW

SIZE	SID NUMBER	DWG NUMBER	REV
A3	N/A	81244	00

STRIP DWG NUMBER/REV	PKG OUTLINE DWG NO.	SCALE	SHEET
N/A	N/A	4:1	1 OF 3